

**Amendments to the Specification:**

Please replace the paragraph beginning at page 10, line 22, with the following redlined paragraph:

For an autoreset implementation that is independent of a VSR, an embodiment of the invention provides use of the status register 306. This technique allows a reset to be automatically performed within the SC 102 itself, without needing the PC host 104 to generate a specific VSR command. Thus, when a mute mode is entered where the SC 102 stops responding to commands from the PC host 104, the communication link between the PC host 104 need not be broken—the SC 102 can internally initiate a reset of the processor 120 and thereby resume communication with the PC host after reset.